

**In the Abstract**

Please replace the abstract with the following amended abstract. A clean copy of the amended abstract is provided on a separate sheet as required.

~~Disclosed is a~~ A microcontroller, comprising: a clock input pin, wherein an input signal from the external circuit is inputted; a clock generator for generating a clock signal by receiving a signal from the clock input pin; a clock output pin for receiving an output signal of the clock generating block means and outputting the output signal; a first switch for transmitting an internal signal of a microcontroller to the clock output pin for using the clock output pin ~~is~~ in a system mode; and a second switch, which is enabled when the clock generating block means is operated in the clock generation mode and disabled when the microcontroller is operated in the system mode.

### **ABSTRACT OF THE DISCLOSURE**

A microcontroller, comprising: a clock input pin, wherein an input signal from the external circuit is inputted; a clock generator for generating a clock signal by receiving a signal from the clock input pin; a clock output pin for receiving an output signal of the clock generating block and outputting the output signal; a first switch for transmitting an internal signal of a microcontroller to the clock output pin for using the clock output pin is in a system mode; and a second switch, which is enabled when the clock generating block is operated in the clock generation mode and disabled when the microcontroller is operated in the system mode.